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Lekatsas, H.; Wolf, W.;

[Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on](#)

Volume 18, Issue 12, Dec. 1999 Page(s):1689 - 1701

Digital Object Identifier 10.1109/43.811316

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- ☐ **2. A code compression scheme for improving SoC performance**
Nikolova, E.G.; Mulvaney, D.J.; Choularas, V.A.; Nunez-Yanez, J.L.;
[System-on-Chip, 2003. Proceedings. International Symposium on](#)
19-21 Nov. 2003 Page(s):35 - 40
Digital Object Identifier 10.1109/ISSOC.2003.1267711
[AbstractPlus](#) | [Full Text: PDF\(524 KB\)](#) IEEE CNF
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- ☐ **3. Compression of embedded system programs**
Kozuch, M.; Wolfe, A.;
[Computer Design: VLSI in Computers and Processors, 1994. ICCD '94. Proceedings., IEEE International Conference on](#)
10-12 Oct. 1994 Page(s):270 - 277
Digital Object Identifier 10.1109/ICCD.1994.331903
[AbstractPlus](#) | [Full Text: PDF\(672 KB\)](#) IEEE CNF
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Effros, M.;
[Signal Processing Magazine, IEEE](#)
Volume 15, Issue 6, Nov. 1998 Page(s):51 - 73
Digital Object Identifier 10.1109/79.733496
[AbstractPlus](#) | [Full Text: PDF\(2704 KB\)](#) IEEE JNL
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- ☐ **5. The MAP1000A VLIM mediaprocessor**
Basoglu, C.; Woobin Lee; O'Donnell, J.S.;
[Micro, IEEE](#)
Volume 20, Issue 2, March-April 2000 Page(s):48 - 59
Digital Object Identifier 10.1109/40.848472
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(120 KB\)](#) IEEE JNL
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- ☐ **6. Code compression for embedded VLIW processors using variable-to-fixed coding**
Yuan Xie, Wolf, W.; Lekatsas, H.;
[Very Large Scale Integration \(VLSI\) Systems, IEEE Transactions on](#)
Volume 14, Issue 5, May 2006 Page(s):525 - 536
Digital Object Identifier 10.1109/TVLSI.2006.876105
[AbstractPlus](#) | Full Text: [PDF\(736 KB\)](#) IEEE JNL
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- ☐ **7. Microprocessors—The first twelve years**
Gupta, A.; Toong, H.-M.D.;
[Proceedings of the IEEE](#)
Volume 71, Issue 11, Nov. 1983 Page(s):1236 - 1256
[AbstractPlus](#) | Full Text: [PDF\(2705 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **8. Predictable Paging in Real-Time Systems: A Compiler Approach**
Puaat, Isabelle; Hardy, Damien;
[Real-Time Systems, 2007. ECRTS '07. 19th Euromicro Conference on](#)
4-6 July 2007 Page(s):169 - 178
Digital Object Identifier 10.1109/ECRTS.2007.25
[AbstractPlus](#) | Full Text: [PDF\(209 KB\)](#) IEEE CNF
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- ☐ **9. A block coding method that leads to significantly lower entropy values for the proteins and coding sections of Haemophilus influenzae**
Sampath, G.;
[Bioinformatics Conference, 2003. CSB 2003. Proceedings of the 2003 IEEE](#)
11-14 Aug. 2003 Page(s):287 - 293
Digital Object Identifier 10.1109/CSB.2003.1227329
[AbstractPlus](#) | Full Text: [PDF\(245 KB\)](#) IEEE CNF
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☐ **10. Code compression using variable-to-fixed coding based on arithmetic coding**

Yuan Xie; Wolf, W.; Lekatsas, H.;

[Data Compression Conference, 2003. Proceedings, DCC 2003](#)

25-27 March 2003 Page(s):382 - 391

Digital Object Identifier 10.1109/DCC.2003.1194029

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☐ **11. Code generation and analysis for the functional verification of microprocessors**

Hosseini, A.; Mavroidis, D.; Konas, P.;

[Design Automation Conference Proceedings 1996, 33rd](#)

3-7 June 1996 Page(s):305 - 310

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